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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Raul-Adrian Cernea et al.  
Title: Non-Volatile Memory and Method With Bit Line to Bit Line Coupled Compensation  
Application No.: 10/667,222 Filing Date: September 17, 2003  
Examiner: HOANG, Huan Group Art Unit: 2818  
Docket No.: SNDK.308US0 Conf. No.: 7021

Certificate of Transmission Under 37 CFR 1.8

I hereby certify that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office on September 16, 2004

Mary S. Bugger  
Signature

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Dear Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, Applicants call the documents listed on the enclosed Form PTO-1449 to the Examiner's attention in this patent application.

This application has a filing date after June 30, 2003. Therefore copies of the U.S. Patent and U.S. Published Patent Applications documents listed on the accompanying Form PTO-1449 are not enclosed.

Citation of these documents shall not be construed as (1) an admission that the documents are prior art with respect to the invention or inventions claimed in this application, (2) a representation that a search has been made (other than as indicated by any cited document), or


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(3) an admission that the cited information is, or is considered to be, material to patentability as defined in § 1.56(b).

This information disclosure statement is submitted under 37 C.F.R. § 1.97(b) and consequently no fee should be required. The Commissioner is authorized, however, to charge any fee that may be required, or to credit any overpayment, against Deposit Account No. 502664 as set forth in the accompanying Transmittal Letter.

Respectfully submitted,

  
Gerald P. Parsons  
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September 16, 2004  
Date

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